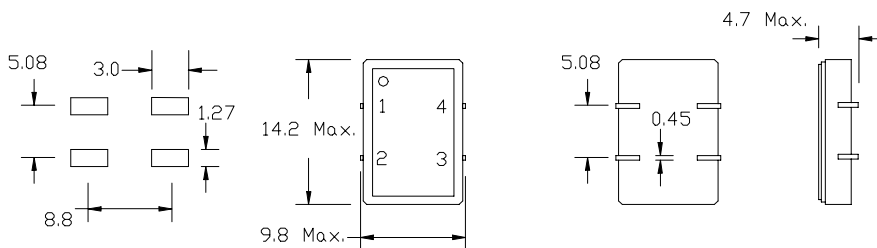


Frequency	1.000 MHz to 200.000 MHz	
Output Level	TTL	HC-MOS
Level	'0' = 0.4 VDC Max., '1' = 2.4 VDC Min.	'0' = 0.1 Vcc Max., '1' = 0.9 Vcc Min.
Duty Cycle	Specify 50% ± 10% or ± 5% See Table	
Rise / Fall Time	8 nS Max. (1 MHz to 40 MHz), 6 nS Max. (40 MHz to 80 MHz), 2.5 nS Max. (80 MHz to 200 MHz) **	
Output Load	Fo < 50 MHz = 10 TTL, Fo > 50 MHz = 5 LSTTL	See Table
Frequency Stability	See Frequency Stability Table (Includes room temperature tolerance and stability over operating temperature)	
Start-up Time	10 mS Max.	
Enable / Disable Time	100 nS Max.	
Supply Voltage	3.3 VDC ±10% or 5.0 VDC ±10%, See Table	
Current	80 mA Max. **	
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Environmental / Tape and Reel	See Application notes for Environmental information and Tape and Reel information	
Package Information	MSL = N.A., Termination = e4	

Tri-State Function	
Pin 1 Open	Enable
Pin 1 ≥ 70% Vdd	Enable
Pin 1 ≤ 30% Vdd	Disable



Dimension Units: mm

Pin Connection
 1 Tri-State
 2 Ground
 3 Output
 4 Vcc

Part Number Guide		Sample Part Number: QKO-3ATA1T - 20.000					
Package	Input Voltage	Operating Temperature	Symmetry (Duty Cycle)	Output	Stability (in ppm)	Enable / Disable	Frequency
QKO -	5 = 5.0 V	A = 0° C to +70° C	T = 45 / 55 Max.	A = 10 TTL / 15 pF HC-MOS	1 = ±100	T= Enable	- 20.000 MHz
	3 = 3.3 V	B = -10° C to +70° C	S = 40 / 60 Max.	B = 15 pF HC-MOS	2 = ±50		
		C = -20° C to +70° C		C = 30 pF	3 = ±25		
		D = -30° C to +75° C		D = 50 pF HC-MOS (<40 MHz)	*4 = ±20		
		E = -40° C to +85° C		E = AC-MOS			

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 4) and Gnd (pin 2) to minimize power supply noise.
 * Not available for all temperature ranges. ** Frequency, supply, and load related parameters.