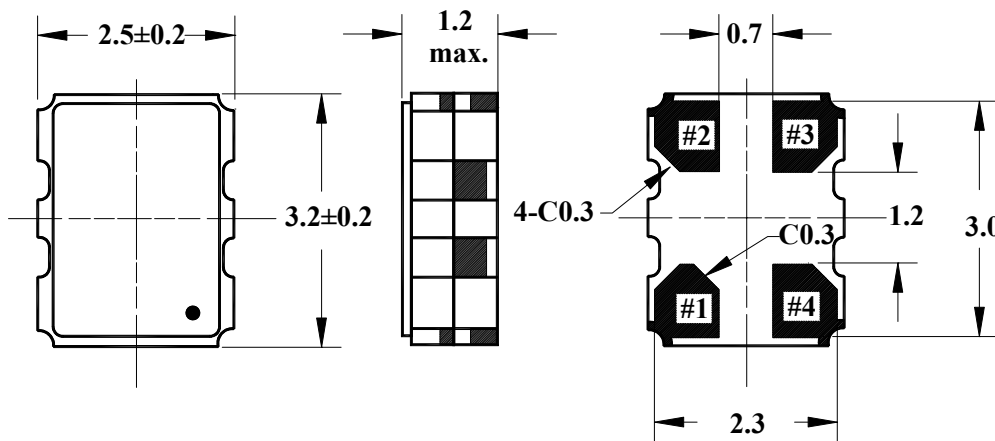


Frequency	1.000 to 60.000 MHz
Output Level	C-MOS
Level	'0'=0.1 Vcc Max., '1'=0.9 Vcc Min.
Duty Cycle	50% ± 5% (@50%Vcc)
Rise/ Fall Time	6 nS Max.
Output Load	15 pF ± 5% (CMOS load)
Enable / Disable	Pin #1 ≤ 0.8 VDC, Output to high impedance state.
Overall Frequency Stability	See Frequency Stability Table (Includes temperature, voltage, load stability, aging for 1 st year at 25°C and initial tolerance at shipping @ 25° C)
Supply Voltage	3.3Vdc ±10%
Current	30 mA Max.
Temperature	
Operating	0° C to +70° C (Standard, see table below for more options)
Storage	-40° C to +85° C

Part Number Guide		Sample Part Number: QDO-3AA4T-20.000				
Part Number	Input Voltage	Temperature Range	Output	Stability	Tristate	Frequency
QDO-	3 = 3.3 VDC	A = 0° C to +70° C	A = 15 pF HC-MOS	1 = ± 100 ppm	T = Enable	- 20.000 MHz
		C = -20° C to +70°	B = 5 LSTTL	2 = ± 50 ppm		
		D = -40° C to +85°		3 = ± 25 ppm		
		F = 0° C to +50° C		4 = ± 20 ppm		

Dimensions: (Unit=mm)



Tri-State Function	
Pin 1 Open	Enable
Pin 1 ≥ 2.2V	Enable
Pin 1 ≤ 0.8 V	Disable

PIN CONNECTIONS

- 1 Tri-State
- 2 GND
- 3 OUTPUT
- 4 Vcc

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