

|                 |  |               |
|-----------------|--|---------------|
| Frequency       | 340.00 KHz to 250.000 MHz                                  |               |
| Output Level    | TTL/HCMOS  | PECL          |
| Level           | '0'= 0.4 Vdc Max., '1'=2.4 Vdc Min.                        | Standard PECL |
| Duty Cycle      | 50% ± 5% or ± 10%  |               |
| Rise/ Fall Time | 5 nS Max. for Vcc = +3.3 Vdc , 10 nS Max. for Vcc=+5 Vdc * |               |
| Output Load     | (Vdd = 3.3V) HCMOS   | Standard PECL |
|                 | <100.000 MHz 15 pF HCMOS Load max                          |               |
|                 | >100.000 MHz 10 pF HCMOS Load max                          |               |
|                 | (Vdd = 5.0V) HCMOS   |               |
|                 | <100.000 MHz 25 pF HCMOS Load max                          |               |
|                 | >100.000 MHz 10 pF HCMOS Load max                          |               |
| Temperature     |  |               |
| Operating       | See Table Operating Temperature                            |               |
| Storage         | -40 °C ~+85 °C   |               |

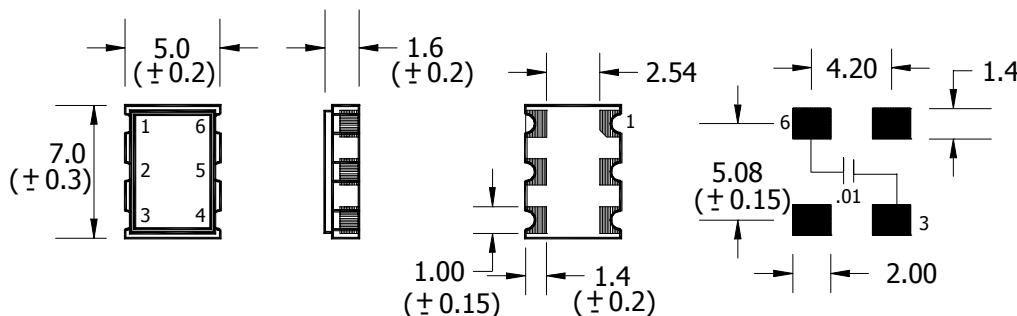
| Part Number Guide |               | Sample Part #:    |                       | QCOP-5ATA1T-20.000 |                    |                    |             |
|-------------------|---------------|-------------------|-----------------------|--------------------|--------------------|--------------------|-------------|
|                   | Input Voltage | Operating Temp    | Symmetry (Duty Cycle) | Output             | Stability (in ppm) | Tristate (Standby) | Frequency   |
| QCOP              | 5 = 5.0V      | A = 0°C ~ +70°C   | T = 45/55 max         | A = TTL/ HCMOS     | 1 = ±100           | T = Enable High    | 20.000 MHz. |
|                   | 3 = 3.3V      | C = -20°C ~ +70°C | S = 60/40 max         | P = PECL           | 2 = ±50            |                    |             |
|                   |               | E = -40°C ~ +85°C |                       |                    |                    | 3 = ±25            |             |

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 6) and Gnd (pin 4) to minimize power supply noise

\* Frequency and output related parameters

| Tri-State Function |         |
|--------------------|---------|
| Pin 1 Open         | Enable  |
| Pin 1 ≥ 2.2V       | Enable  |
| Pin 1 ≤ 0.8V       | Disable |

DIMENSION UNITS: mm



PIN CONNECTIONS

- 1 NC / ED
- 2 NC
- 3 GROUND
- 4 OUTPUT
- 5 NC (Program)
- 6 Vcc

**QVS TECH INC**

6965 El Camino Real, Ste 105 Carlsbad, CA 92009 Phone: 760-929-8677 Fax: 760-929-8077

email: [sales@qvstech.com](mailto:sales@qvstech.com)

Specifications subject to change without notice (Rev A)